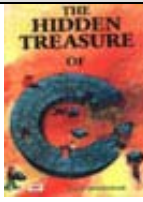


Brief Curriculum Vitae

Dr. Rajiv V. Dharaskar

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Educational Qualifications			
Ph.D. (Computer Engineering, Faculty of Engineering & Technology)	M.Tech. (Computers)	P.B. Diploma (Comp. Science & Applications)	
M.Phil. (Mathematics)	M.Sc. (Mathematics)	B.Sc. (Physics, Chemistry., Maths)	
M.A. I (Economics with Mathematical Economics, Econometrics, Statistics)	Additional B.A. (Economics)		
Research Papers (Total 226)		Keynote Address / Reviewer / Session Chair	
Journal Research Papers	62	Keynote / Invited Speaker at International / National Conferences	27
International Conference Papers	87	Reviewer / Editor	29
National Conference Papers	77	General Chair / Publicity Chair / Regular Chair / Session Chair / Organizing Committee / Steering Committee / Scientific Committee Member of International / National Conferences	17
Books		Guide	
"The Hidden Treasure of C" from BPB Publications, New Delhi. (1994) More than 25, 000 copies are in circulation throughout the world		Ph.D.	13 (10 registered, 3 submitted and 3 students have applied for registration, 7 foreign student's registration is under consideration)
		M.Tech.	32
		M.Phil.	5
		B.E.	More than 80
Position Held (in last 27 Years)			
Director, MPGI Integrated Campus, Matoshri Pratishthan Group of Institute, Nanded (An Integrated Campus is Mini University concept of AICTE. Managing 3 institutes and 3 Principals are working under me)			
Professor and Head, UG / PG Department of Computer Science & Engineering, GH Raison College of Engineering, An Autonomous Institute, Nagpur			
Associate Professor and Head (System Analyst), Department of Computer Technology, Manoharbai Patel Institute of Engineering & Technology(MIET), Gondia			
Lecturer (Assistant Professor), Department of Mathematics, MIET, Gondia			
Administrative / Development / Technical / University / Autonomous Institute Experience			
Academic Council, Chairman BOS, Chairman, Member of PG Moderation Committee, 32/5 committee, Selection Committee, Permanent Affiliation Committee, Local Enquiry Committee, Syllabus Design committee for BE & MTech courses, Ph.D. guide / examiner to various universities, Framed Rules and Regulations for autonomous institute and deemed university, Coordinator for MTech accreditation (NBA), Government research funding for various minor and major projects, TEQIP 1, 1.2 activities, As a Director Integrated Campus introduced Administrative reforms, Fiscal Policies and various measures, Established of T&P division, Organized International (IEEE) and National Conferences, Active member of various professional bodies like CSI, IEEE, Collaborative activities and MOUs, Software development, Established laboratories, Coordinate many student centric activities like software / technical exhibitions, NSS etc., Guided and motivated MTech students for research and they published more than 300 papers, Use of e-learning technology for teaching many computer subjects, Curriculum Development of various courses like MTech, BE, MCA etc, Technical skills: Knows hundreds of computer languages, software, products and guided projects for BE, MTech, PhD etc.			
Special Recognition			
Shortlisted for the position of Vice Chancellor by the VC Search Committee for Symbiosis International University (SIU) Pune, headed by former CSIR Director Hon. Dr. Raghunath A. Mashelkar and Hon. Vice Chancellor of Delhi University Dr Dinesh Singh.			
Shortlisted for the position of Vice Chancellor , Maharana Pratap University, Jaipur			
Personal Details			
Full Name	Rajiv Vasant Rao Dharaskar		
Date of birth and Age	04 Aug 1960 , 51 years		
Permanent Address	Dharaskar Nursing home, Railtoly, Gondia, Maharashtra 441614		
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